



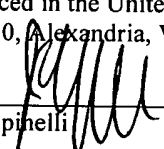
ATTORNEY'S DOCKET NO: S1022.80707US00
(formerly S01022.80707.US)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ferruccio Frisina
Serial No.: 09/925,080
Filed: August 8, 2001
For: METHOD OF MANUFACTURING AN INTEGRATED EDGE
STRUCTURE FOR HIGH VOLTAGE SEMICONDUCTOR DEVICES,
AND RELATED INTEGRATED EDGE STRUCTURE
Confirmation No.: 3073
Examiner: Luu, Chuong A.
Art Unit: 2825

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

The undersigned hereby certifies that this document is being placed in the United States mail with first-class postage attached, addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on December 15, 2003.


Jeanne Spinelli

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

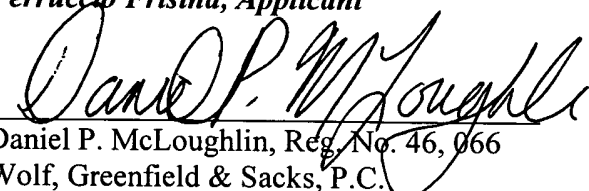
PETITION FOR A TWO MONTH EXTENSION OF TIME

A two (2) month extension of time, to and including December 15, 2003, is requested for response to the Office Action of July 15, 2003.

The check enclosed includes the fee of \$420.00 for the extension. If the amount is insufficient, please charge our Deposit Account No. 23/2825. A duplicate of this Petition is enclosed.

Respectfully submitted
Ferruccio Frisina, Applicant

By:


Daniel P. McLoughlin, Reg. No. 46, 066
Wolf, Greenfield & Sacks, P.C.
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Attorney for Applicant

Docket No.: S1022.80707US00
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Date: December 15, 2003
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